Memory Module Specification

KHX5300S2LLK2/2G

2GB (1GB 128M x 64-Bit x 2 pcs.)
PC2-5300 CL4 200-Pin SODIMM Kit

DESCRIPTION:

Kingston's KHX5300S2LLK2/2G is a kit of two 128M x 64-bit 1GB (1024MB) DDR2-667 CL4 SDRAM (Synchronous DRAM) memory modules, based on sixteen 64M x 8-bit DDR2 FBGA components (per module). Total kit capacity is 2GB (2048MB). Each pair has been tested to run at DDR2 667MHz at low latency timing of 4-4-4-12 at 1.8V. The SPDs are programmed to JEDEC latency timing of 4-4-4-12 at 1.8V. Each 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

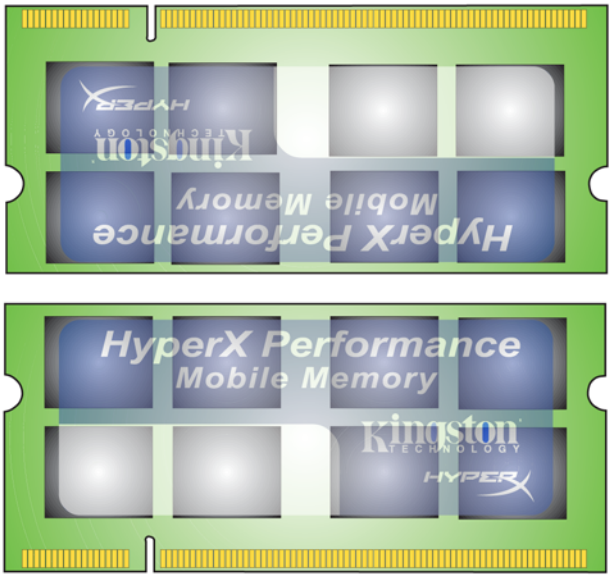
- ☑ Power supply : Vdd: 1.8V ± 0.1V, Vddq: 1.8V ± 0.1V
- ☑ Double-data-rate architecture; two data transfers per clock cycle
- ☑ Bidirectional data strobe(DQS)
- ☑ Differential clock inputs(CK and CK)
- ☑ DLL aligns DQ and DQS transition with CK transition
- ☑ Programmable Read latency 3, 4 (clock)
- ☑ Burst Length: 4, 8 (Interleave/nibble sequential)
- ☑ Programmable Burst type (sequential & interleave)
- ☑ Timing Reference: 4-4-4-12 at +1.8V
- ☑ Edge aligned data output, center aligned data input
- ☑ Auto & Self refresh, 7.8us refresh interval (8K/64ms refresh)
- ☑ Serial presence detect with EEPROM
- ☑ PCB : Height 1.180” (30.00mm), double sided component

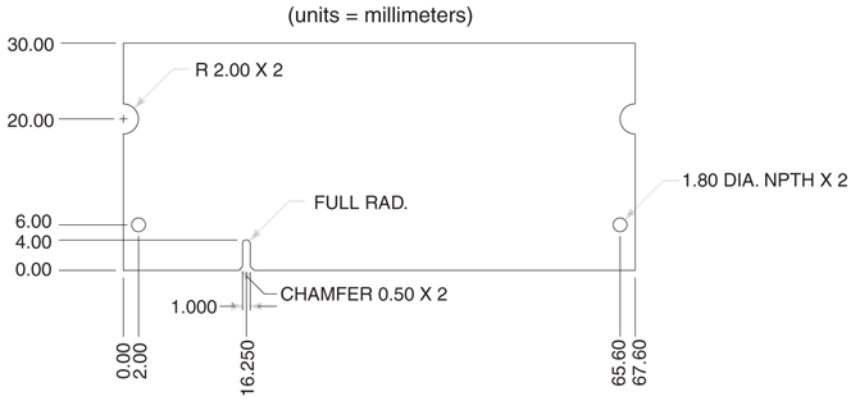
PERFORMANCE:

☑ Clock Cycle Time (tCK) CL=4	3ns (min.) / 8ns (max.)
☑ Row Cycle Time (tRC)	60ns (min.)
☑ Refresh to Active/Refresh Command Time (tRFC)	105ns
☑ Row Active Time (tRAS)	36ns (min.) / 70,000ns (max.)
☑ Single Power Supply of	+1.8V (+/- .1V)
☑ Power	1.800 W (operating per module)
☑ UL Rating	94 V - 0
☑ Operating Temperature	0° C to 55° C
☑ Storage Temperature	-55° C to +125° C

Preliminary

MODULE DIMENSIONS:





For more information, go to www.kingston.com

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